

Fall 19 ESE Course Schedule										
Subject	CRS	Course Title	Cmp	Sctn	Days	Start Time	End Time	Building	Room	Instructor
ESE	118	Digital Logic Design	LEC	01	MF	1:00 PM	2:20 PM	LGT ENGR LAB	102	Dmitri Donetski
ESE	118	Digital Logic Design	LAB	L01	M	3:55 PM	6:55 PM	HVY ENGR LAB	235	Dmitri Donetski
ESE	118	Digital Logic Design	LAB	L03	TU	7:00 PM	10:00 PM	HVY ENGR LAB	235	Dmitri Donetski
ESE	118	Digital Logic Design	LAB	L04	W	7:00 PM	10:00 PM	HVY ENGR LAB	235	Dmitri Donetski
ESE	118	Digital Logic Design	LAB	L05	TU	12:55 PM	3:55 PM	HVY ENGR LAB	235	Dmitri Donetski
ESE	121	Introduction to Audio Systems	LEC	02	TUTH	8:30 AM	9:50 AM	JAVITS LECTR	102	Jan Folkson
ESE	122	Discrete Math for Engineers	LEC	01	TUTH	1:00 PM	2:20 PM	STALLER CTR	M0113	Sangjin Hong
ESE	123	Intro to Elec and Compu Engrng	LEC	01	MW	5:30 PM	6:50 PM	JAVITS LECTR	102	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L01	TH	3:55 PM	6:55 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L02	TU	3:55 PM	6:55 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L03	W	2:20 PM	5:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L04	TH	8:20 AM	11:20 AM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L05	TH	11:25 AM	2:25 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L06	TU	9:55 AM	12:55 PM	LGT ENGR LAB	283	David Westerfeld
ESE	211	Electronics Laboratory A	LEC	01	M	10:00 AM	10:53 AM	FREY HALL	301	Vera Gorfinkel
ESE	211	Electronics Laboratory A	LAB	L01	TU	7:00 PM	10:00 PM	LGT ENGR LAB	283	Vera Gorfinkel
ESE	211	Electronics Laboratory A	LAB	L02	TH	7:00 PM	10:00 PM	LGT ENGR LAB	283	Vera Gorfinkel
ESE	224	Cmptr Tchnq for Electr Dsgn II	LEC	01	TUTH	1:00 PM	2:20 PM	MELVILLE LBR	W4550	Xin Wang
ESE	271	Electrical Circuit Analysis	LEC	01	MW	8:30 AM	9:50 AM	ENGINEERING	145	Sergey Suchalkin
ESE	280	Embedded Systems Design I	LEC	01	TUTH	4:00 PM	5:20 PM	ENGINEERING	145	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L01	W	8:55 AM	11:55 AM	LGT ENGR LAB	230	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L02	W	2:25 PM	5:25 PM	LGT ENGR LAB	230	Kenneth Short



ESE/C	346	Computer FULL	LEC	01	MF	1:00 PM	2:20 PM	FREY HALL	201	Thomas Robertazzi
ESE	355	VLSI System Design	LEC	01	MWF	8:00 AM	8:53 AM	ENGINEERING	105	Alexa Doboli
ESE	355	VLSI System Design	LAB	L01	TU	9:55 AM	12:55 PM	LGT ENGR LAB	246	Alexa Doboli
ESE	355	VLSI System Design	LAB	L02	TH	12:55 PM	3:55 PM	LGT ENGR LAB	246	Alexa Doboli
ESE	356	Digi Sys Specifica & Modeling	LEC	01	TU	4:00 PM	6:50 PM	EARTH&SPACE	181	Sangjin Hong
ESE	358	Computer Vision	LEC	01	TUTH	10:00 AM	11:20 AM	FREY HALL	112	Murali Subbarao
ESE	372	Electronics	LEC	01	MW	2:30 PM	3:50 PM	FREY HALL	201	Ridha Kamoua
ESE	372	Electronics	REC	R01	RECM	11:00 AM	11:53 AM	FREY HALL	201	Ridha Kamoua
ESE	440	Senior Design I	LEC	01	MW	4:00 PM	5:20 PM	HUMANITIES	1003	Harbans Dhadwal
ESE	441	Engineering Design II	LEC	01	MW	4:00 PM	5:20 PM	HUMANITIES	1003	Harbans Dhadwal
ESE	475	Undergrad Teaching Practicum	TUT	T36	APPT	1:00 AM	1:00 AM	TBA	TBA	Choose One
ESE	476	Instructional Lab Practicum	TUT	T35	APPT	1:00 AM	1:00 AM	LGT ENGR LAB	201	Choose One
ESE	488	Internship Electr/Comp Engrng	TUT	T75	APPT	1:00 AM	1:00 AM	TBA	TBA	Choose One
ESE	494	Honors Seminar on Research	LEC	01	M	10:00 AM	10:53 AM	FREY HALL	216	Vibha Mane
ESE	495	Honors Research Project	TUT	T35	APPT	1:00 AM	1:00 AM	LGT ENGR LAB	201	Choose One
ESE	499	Research in Electrical Science	TUT	T37	APPT	1:00 AM	1:00 AM	TBA	TBA	Choose One
7.9.19 update										